INT TERMINAL INTERRUPT CONTROLLER

SIGNAL "h"

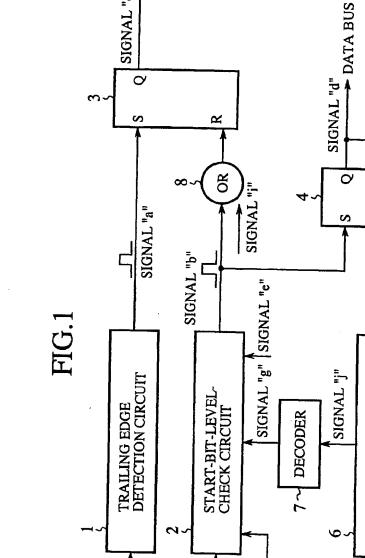
SIGNAL "e"

BIT COUNTER

RECEPTION CLOCK GENERATING CIRCUIT

SIGNAL "F"

SIGNAL "c".



SIGNAL "c"

RXD INPUT LEVEL

SIGNAL "c"

Tactor hadanded

FIG.2

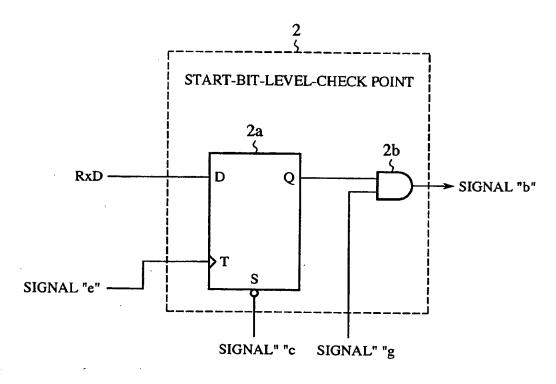


FIG.5 (PRIOR ART)

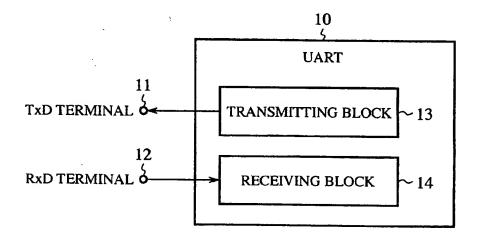
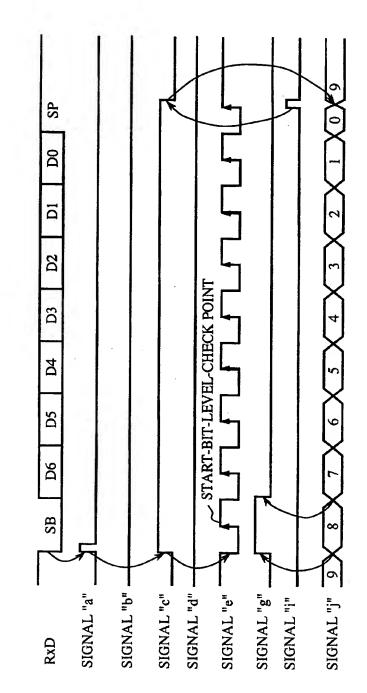


FIG. 3



SP 2 百 - START-BIT-LEVEL-CHECK POINT 22 <u>D</u>3 ጀ NOISE SIGNAL "a" SIGNAL "b" SIGNAL "c" SIGNAL "g" SIGNAL "d" SIGNAL "e" SIGNAL "¡" SIGNAL "j" RxD

FIG.6 (PRIOR ART)

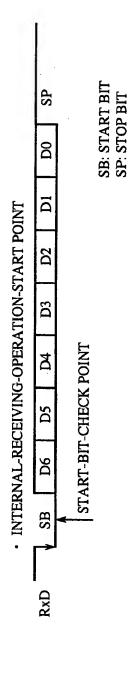


FIG.7 (PRIOR ART)

